

TIMETABLE OF ISSCC 2007 SESSIONS

Sunday, February 11th				ISSCC 2007 TUTORIALS			
T1: Embedded Power-Management Circuits (12:30pm; 2:30pm)		T6: CMOS Front-End Circuit Design (12:30pm; 2:30pm)		T7: Vector Processing as an Enabler for Software-Defined Radio in Handsets (8:00am; 10:00am)			
T2: Continuous-Time $\Delta\Sigma$ Data Converters (8:00am; 10:00am)		T8: Organic-Transistor Circuit Design (8:00am; 10:00am)		T9: Radio Design for MIMO Systems with an Emphasis on IEEE 802.11n (12:30pm; 2:30pm)			
T3: Dealing with Issues in VLSI Interconnect Scaling (12:30pm; 2:30pm)		T10: Fundamentals of Electronic Dispersion Compensation (8:00am; 10:00am)					
T4: Dynamic Offset-Cancellation Techniques in CMOS (8:00am; 10:00am)							
T5: ECC for Memories (12:30pm; 2:30pm)							
MEMORY DESIGN FORUM				CIRCUIT DESIGN FORUM			
8:00AM		F1: Non-Volatile Memory Circuit Design & Technology		F2: Design of 3-D Chipstacks		F3: Power Amplifiers & Transmitter Architectures	
ISSCC 2007 SPECIAL-TOPIC EVENING SESSIONS							
7:30PM		SE1: Digitally Enhanced Analog & RF		SE2: Circuit Design in the Year 2012			
Monday, February 12th				ISSCC 2007 PAPER SESSIONS			
8:30AM		Session 1: Plenary Session		Session 4:		Session 5:	
1:30PM		Session 2: Optical Communications		TD: Emerging Devices and Circuits		RF Building Blocks	
5:15PM		Social Hour: Poster Session - DAC and ASSC Student Design Contest Winners					
ISSCC 2007 DISCUSSIONS SESSIONS							
8:00PM		SE3: Last Mile Access Options: PON/DSL/Cable/Wireless		E1: Ultimate Limits of Integrated Electronics		SE4: Automotive Signal Processing Technologies	
Tuesday, February 13th				ISSCC 2007 PAPER SESSIONS			
8:30AM		Session 8: Biomedical Devices		Session 9: Clocking		mm-Wave Transceivers and Building Blocks	
1:30PM		Session 15: Multimedia and Parallel Signal Processors		Session 16: Power Distribution and Management		Session 17: Analog Techniques & PLLs	
5:15PM		Social Hour: Poster Session - DAC-ISSCC and ASSC Student Design Contest Winners					
ISSCC 2007 DISCUSSIONS SESSIONS							
8:00PM		SE5: Highlights of IEDM		SE6: Secure Digital Systems		E2: Digital RF - Fundamentally a New Technology or Just Marketing Hype?	
Wednesday, February 14th				ISSCC 2007 PAPER SESSIONS			
8:30AM		Session 21: Sensors & MEMS		Session 22: Digital Circuit Innovations		Session 23: Broadband RF and Radar	
1:30PM		Session 27: DRAM and eRAM		Session 28: Image Sensors		Session 29: Analog & Power Management Techniques	
Thursday, February 15th				ISSCC 2007 SHORT COURSE			
8:00AM		Analog, Mixed-Signal, and RF Circuit Design in Nanometer CMOS					
IMAGER DESIGN FORUM		ATAC DESIGN FORUM		MICROPROCESSOR FORUM		CIRCUIT DESIGN FORUM	
8:00AM		F4: Noise in Imaging Systems		F5: Automotive Bus Systems		F6: Adaptive Techniques for Dynamic Processor Optimization	
		BOOK DISPLAY					
Monday, February 12th, 12:00PM - 8:00PM		Tuesday, February 13th, 10:00AM - 8:00PM		Wednesday, February 14th, 10:00AM - 3:00PM			